

# **CMPE 315: Principles of VLSI Design**

## **Lab Cover Page**

**Lab #** : \_\_\_\_\_  
**Lab Title** : \_\_\_\_\_

**Name** : \_\_\_\_\_  
**Section** : \_\_\_\_\_

**Date Submitted** : \_\_\_\_\_

**TA / Grader Use Only:**

**Late Submission Deduction (20% per day late):**

**Other Deductions:**

**Final Lab Grade:**

**Comments to student:**